

## Firmware Engineer

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Company: Rambus

Location: Burnaby

Category: computer-and-mathematical

### Responsibilities

- Design and implement software for embedded devices and systems from the requirements stage to commercial deployment of the product.
- Analyze and enhance efficiency, stability and scalability of system resources.
- Design, develop, code, test and debug system software.
- Integrate and validate new product designs.
- Develop, test, and debug software using pre silicon development platforms.
- Support embedded software during post silicon bring-up effort.
- Interface and work closely with IP designers, integrators, hardware designers, and validation engineers.
- Create design documents for software architectures and algorithms.
- Participate in code and software design reviews, to ensure adherence to coding convention and best practices.
- Provide training, mentorship and coaching to less experienced software developers.

### Qualifications

- BS or higher degrees preferred (Computer Science, Computer Engineering or Electrical Engineering with Software experience).
- Minimum 5 years experience in developing and debugging embedded software preferred
- Strong C/C++ design and programming skills.
- Experience developing device drivers and low-level code for hardware peripherals such as timers, UART, GPIO, DMA, and serial interfaces.

- Experience with real time OS techniques including preemptive multi-tasking schedulers, multi-threaded execution, semaphores, mutexes, and interrupts.
- Proficient in writing performance optimized code for CPU and memory constrained environments.
- Experience with memory RAS (Reliability, Availability and Serviceability) features, including error detection and correction.
- Experience with embedded security features, such as hardware root of trust, secure boot, and key authentication.
- Ability to follow guidance from more senior members of the team.
- Excellent written and oral communication skills.
- Ability to work independently and in small teams.

#### Desired Skills:

- Experience writing software for embedded ARM or RISCv platforms, System-On-Chip (SoC), or x86 platforms.
- Scripting languages such as Python.
- Experience using DDR technologies such as PMIC, RCD, SPD, DDR4, DDR5, and DDR PHYs.
- Experience with PCIe, CXL, NVMe, I3C, QSPI.
- Experience writing software to implement protocols such as SPDM, SMBus, Xmodem.
- Hardware bring-up experience using JTAG debug tools.
- Experience with PCB and full system debug.
- Experience working in lean development organizations and/or startups.

#### About Rambus

With 30 years of innovation and semiconductor expertise, Rambus leads the industry with products and solutions speed performance, expand capacity and improve security for today's most demanding applications. From data center and edge to artificial intelligence and automotive, our interface and security IP, and memory interface chips enable SoC and system designers to deliver their vision of the future.

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